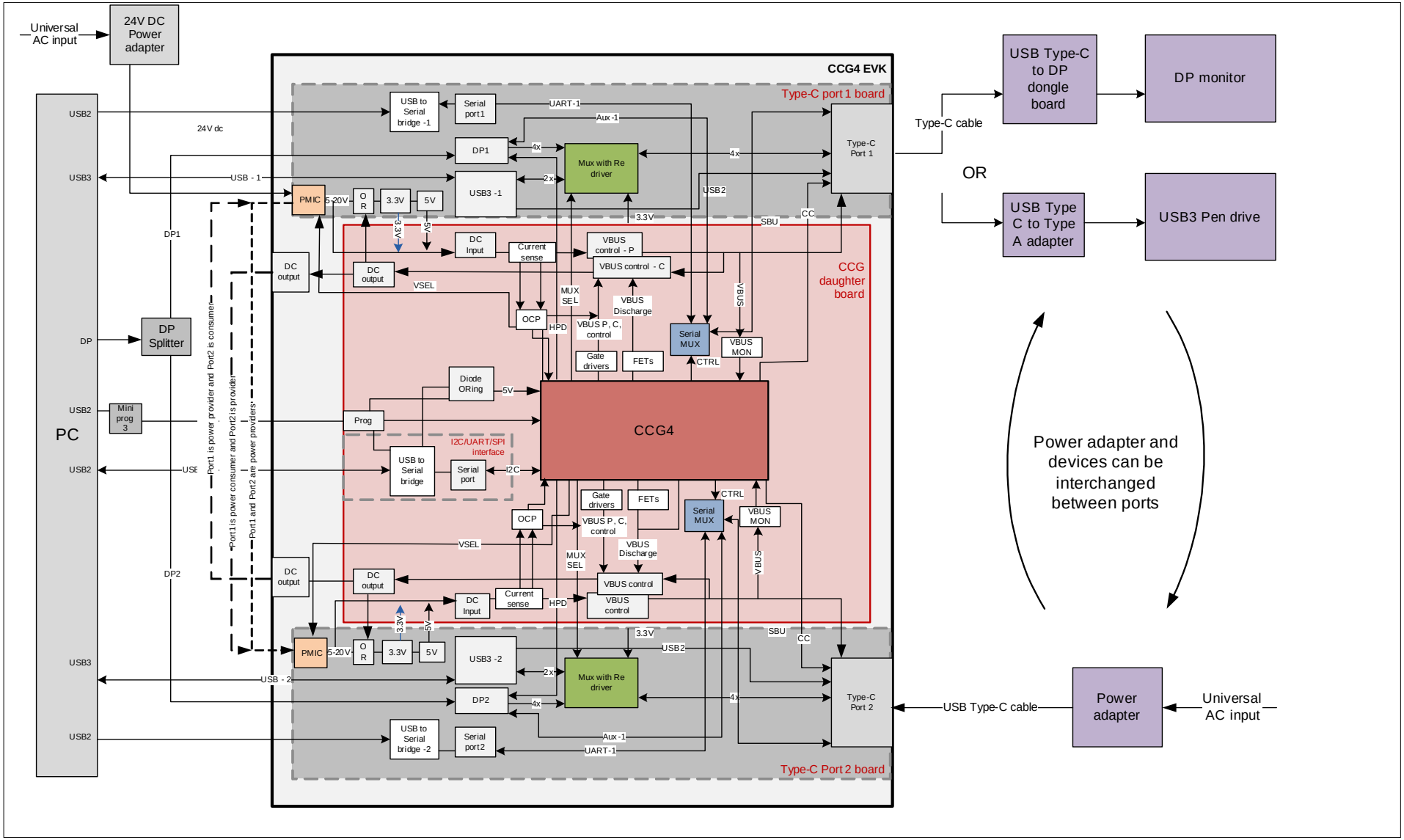


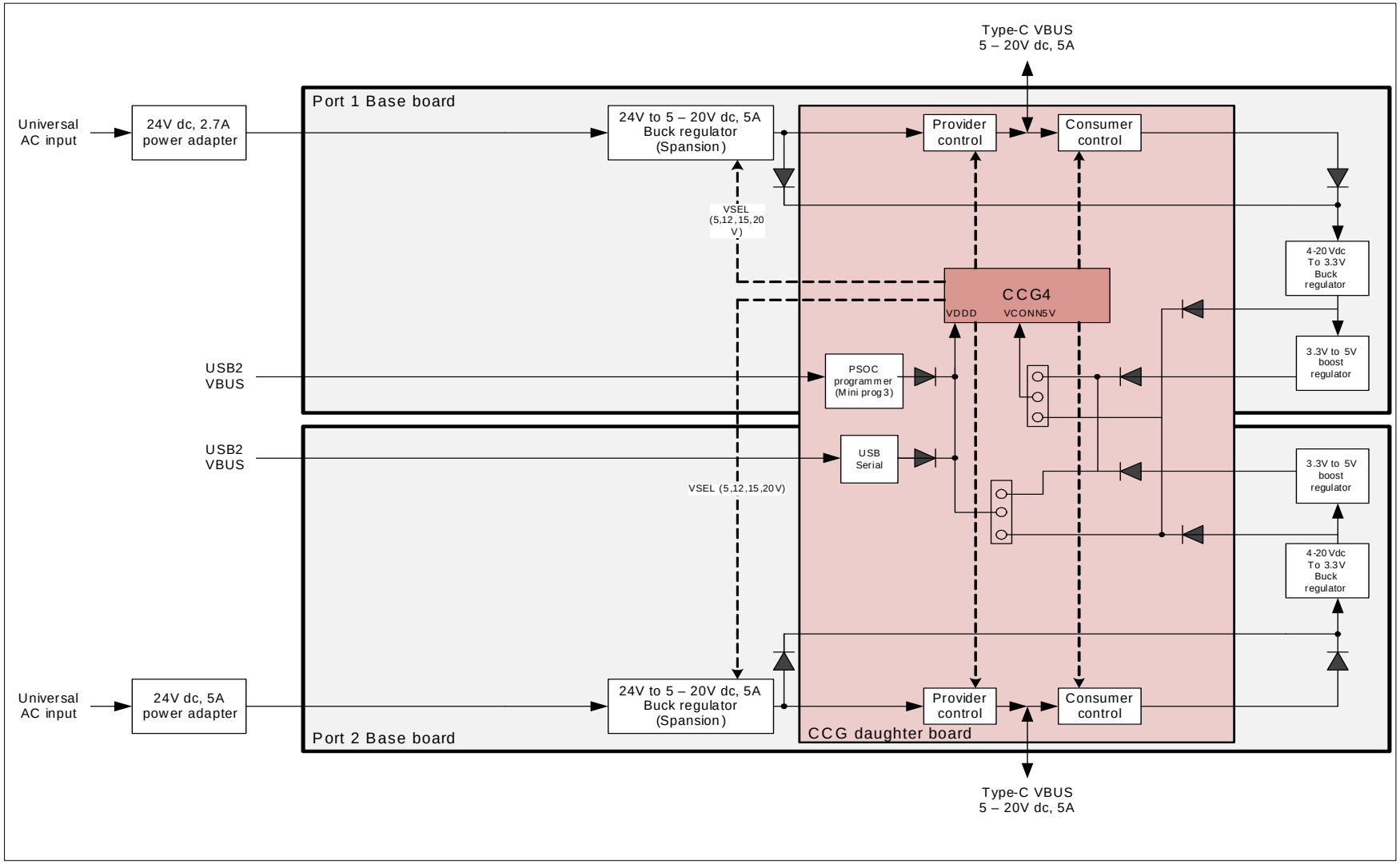
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Title
CCG4 DAUGHTER BOARD - OVERALL DIAGRAM

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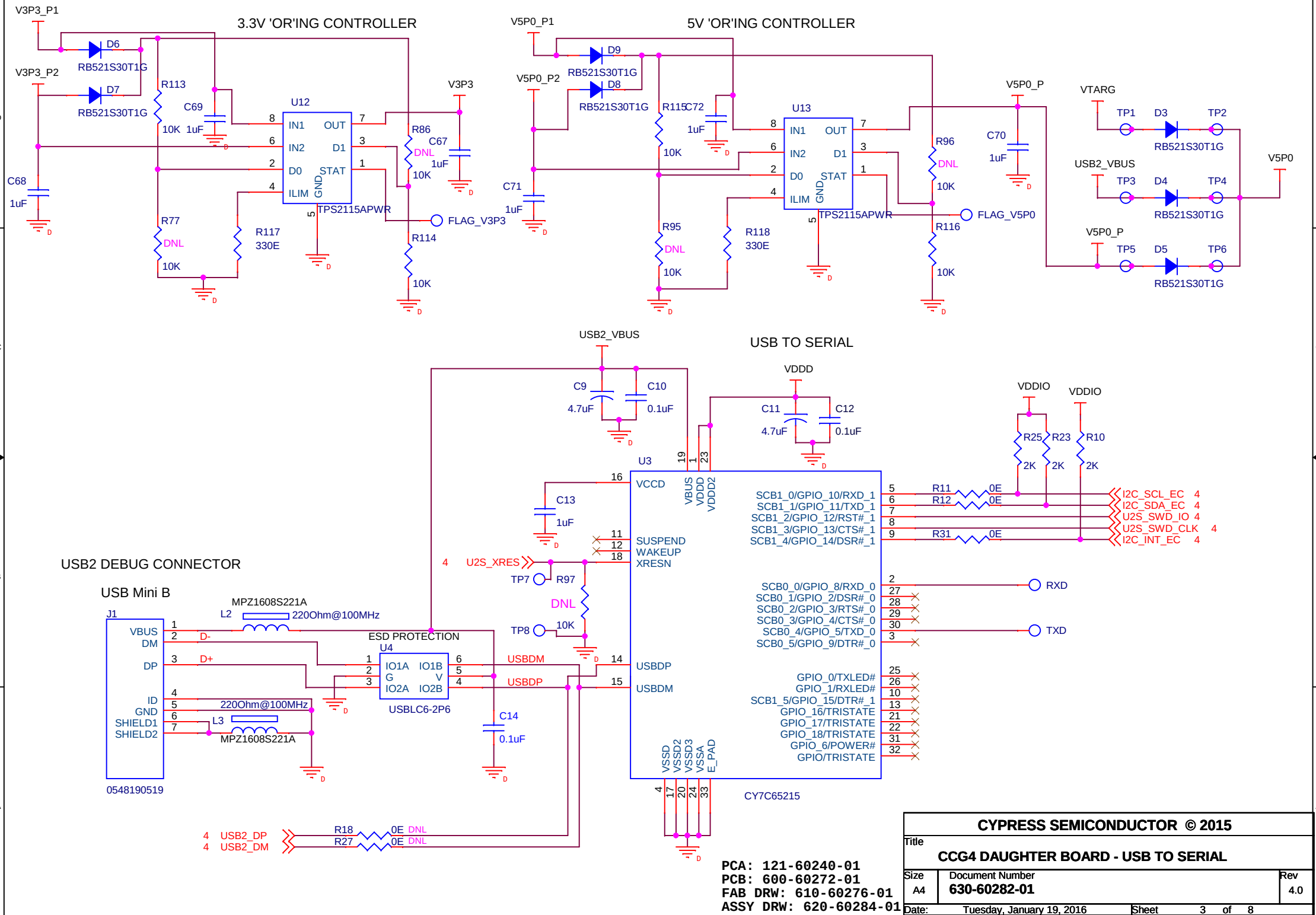
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Title	**CCG4 DAUGHTER BOARD - POWER DISTRIBUTION**	
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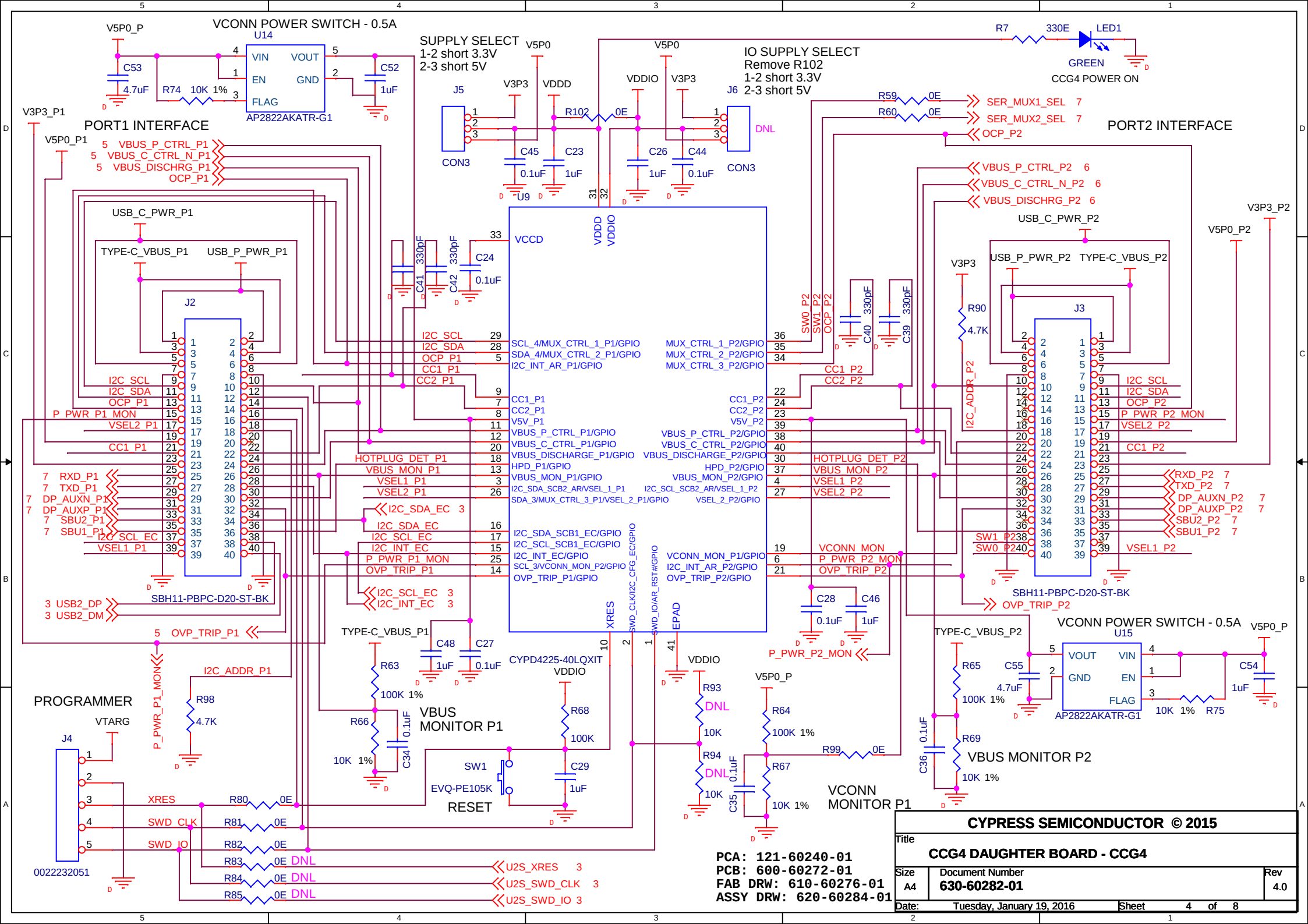
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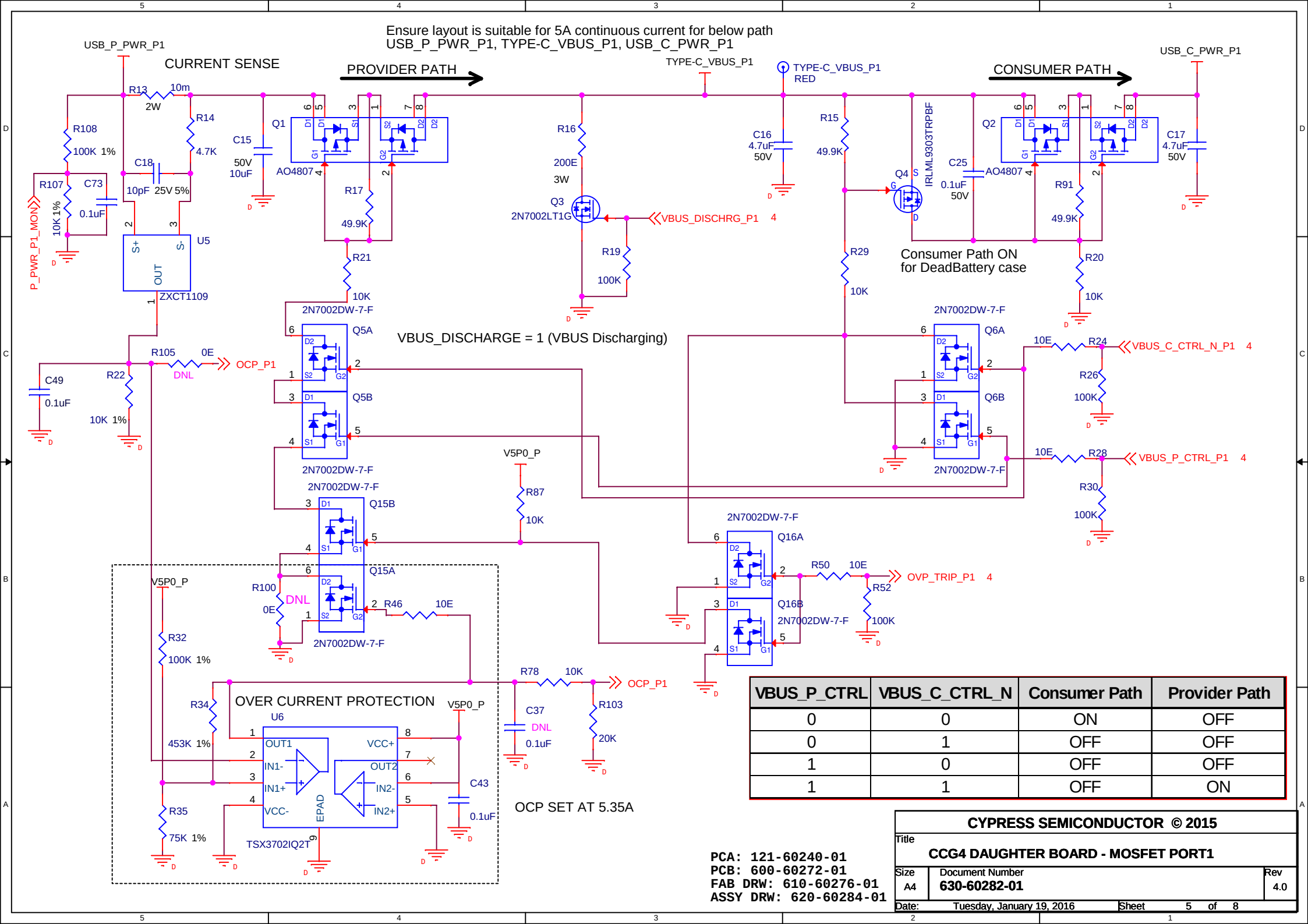
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Title: **CCG4 DAUGHTER BOARD - USB TO SERIAL**

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VCONN POWER SWITCH - 0.5A
U14
AP2822AKATR-G1
SUPPLY SELECT
1-2 short 3.3V
2-3 short 5V
IO SUPPLY SELECT
Remove R102
1-2 short 3.3V
2-3 short 5V
R7 330E LED1
GREEN
CCG4 POWER ON
PORT1 INTERFACE
PORT2 INTERFACE
CYPD4225-40LQXIT
VDDIO
VCCD
SCL_4/MUX_CTRL_1_P1/GPIO
SDA_4/MUX_CTRL_2_P1/GPIO
I2C_INT_AR_P1/GPIO
MUX_CTRL_1_P2/GPIO
MUX_CTRL_2_P2/GPIO
MUX_CTRL_3_P2/GPIO
CC1_P1
CC2_P1
V5V_P1
VBUS_P_CTRL_P1/GPIO
VBUS_C_CTRL_P1/GPIO
VBUS_DISCHARGE_P1/GPIO
HPD_P1/GPIO
VBUS_MON_P1/GPIO
I2C_SDA_SCB2_AR/VSEL_1_P1
SDA_3/MUX_CTRL_3_P1/VSEL_2_P1/GPIO
CC1_P2
CC2_P2
V5V_P2
VBUS_P_CTRL_P2/GPIO
VBUS_C_CTRL_P2/GPIO
VBUS_DISCHARGE_P2/GPIO
HPD_P2/GPIO
VBUS_MON_P2/GPIO
I2C_SCL_SCB2_AR/VSEL_1_P2
VSEL_2_P2/GPIO
I2C_SDA_SCB1_EC/GPIO
I2C_SCL_SCB1_EC/GPIO
I2C_INT_EC/GPIO
SCL_3/VCONN_MON_P2/GPIO
OVP_TRIP_P1/GPIO
VCONN_MON_P1/GPIO
I2C_INT_AR_P2/GPIO
OVP_TRIP_P2/GPIO
SWD_CLK/I2C_CFG_EC/GPIO
SWD_IO/AR_RST#/GPIO
XRES
EPAD
SBH11-PBPC-D20-ST-BK
PROGRAMMER
J4
0022232051
VTARG
VBUS MONITOR P1
VBUS MONITOR P2
VCONN MONITOR P1
RESET
SW1
EVQ-PE105K
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VBUS_P_CTRL	VBUS_C_CTRL_N	Consumer Path	Provider Path
0	0	ON	OFF
0	1	OFF	OFF
1	0	OFF	OFF
1	1	OFF	ON

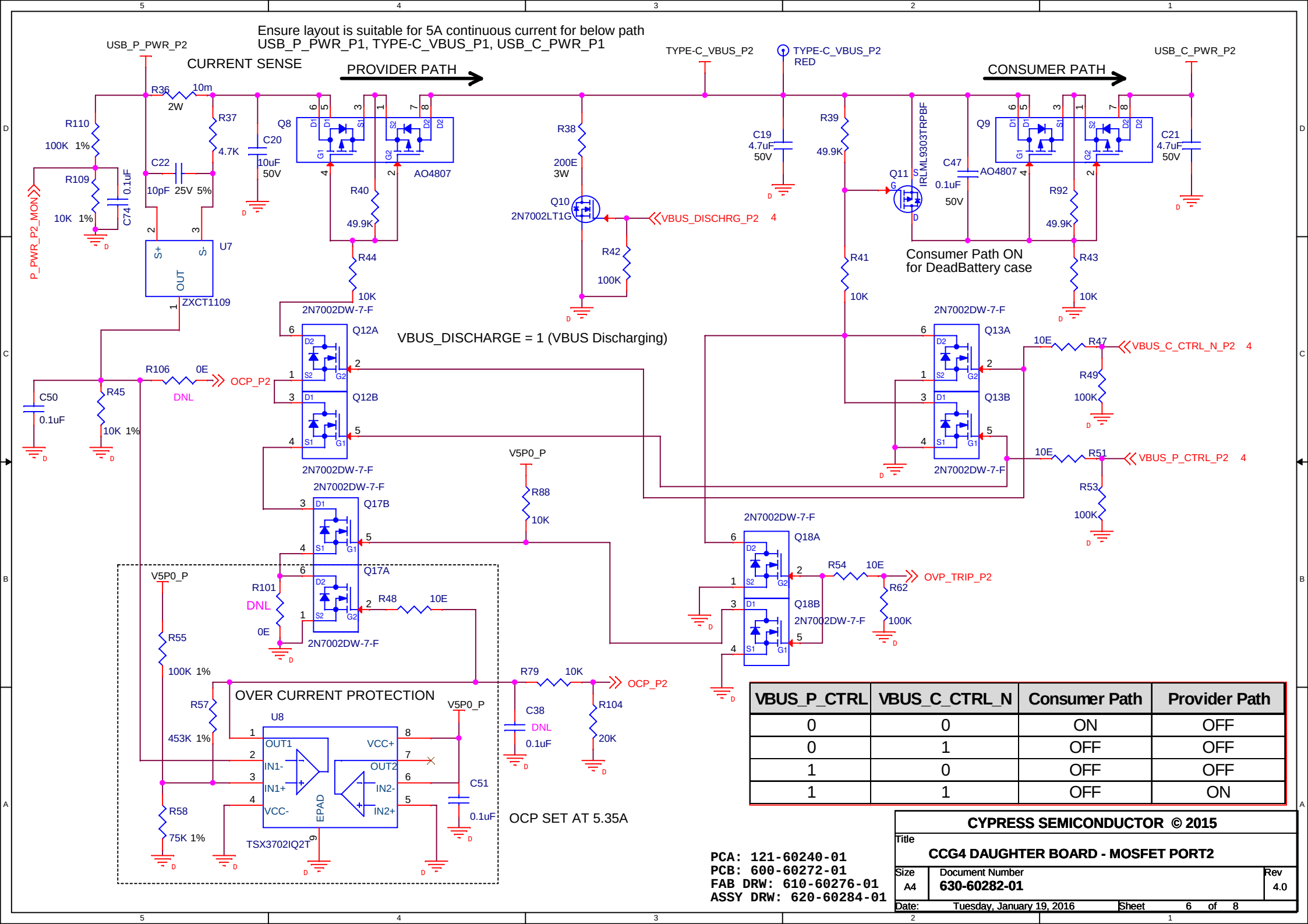
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Title: **CCG4 DAUGHTER BOARD - MOSFET PORT1**

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VBUS_P_CTRL	VBUS_C_CTRL_N	Consumer Path	Provider Path
0	0	ON	OFF
0	1	OFF	OFF
1	0	OFF	OFF
1	1	OFF	ON

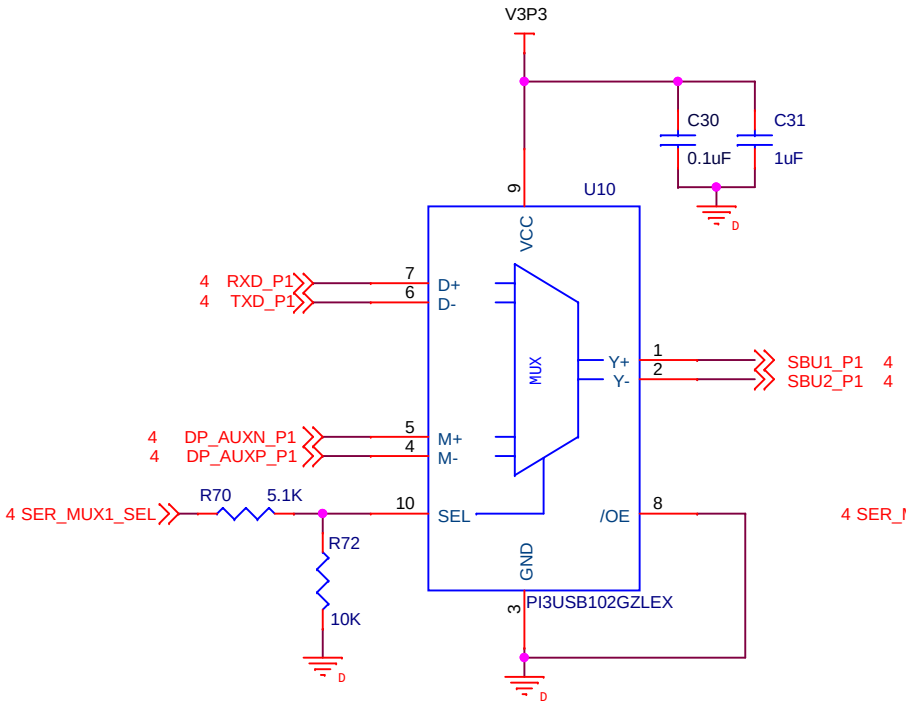
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Title: CCG4 DAUGHTER BOARD - MOSFET PORT2

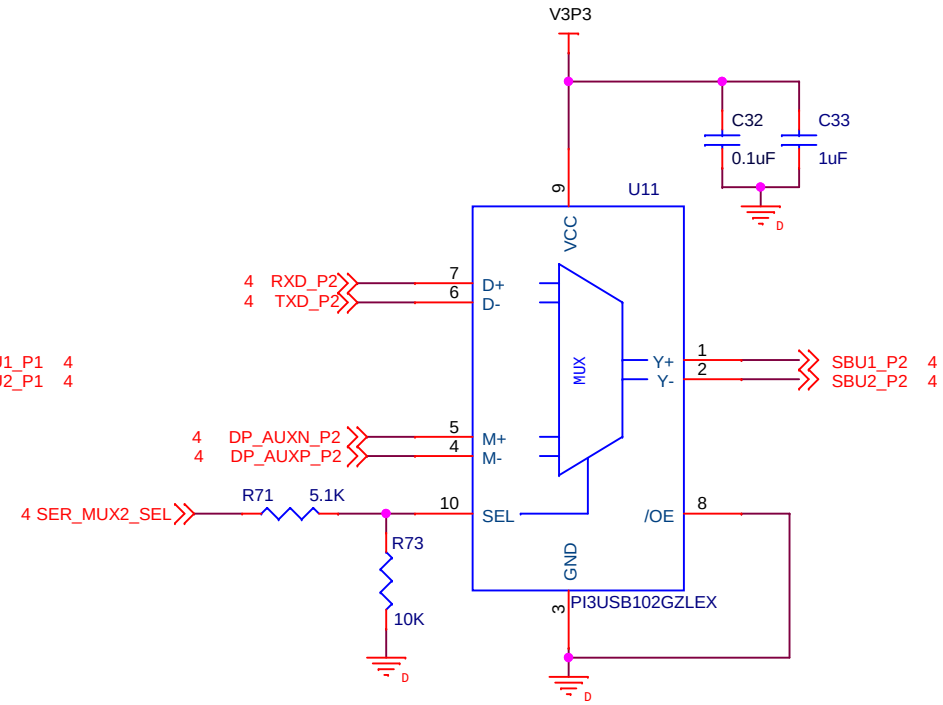
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SERIAL MUX - PORT 1



SERIAL MUX - PORT 2



SBU OUTPUT SELECTION	
MUX SEL	**OUTPUT**
0	UART
1	DP AUX

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Revision	Description of the change
2.0	1. USB TO SERIAL CHIP RESISTOR CONNECTION CHANGES FOR I2C LINES (3 SIGNALS) AND THEIR PULL UP CHANGED FROM V3P3 TO VDDIO 2. R15, R39 VALUE CHANGED AS 49.9K 3. R29, R41 VALUE CHANGED AS 20K 4. R33, R56 REMOVED 5. Q16, Q17 CONNECTIONS CHANGED 6. R78, R79 VALUE CHANGED AS 10K 7. R103, R104 10K PULL DOWN RES ADDED 8. R63, R64, R65 CHANGED FROM 11K TO 100K 9. R69 FROM 1K TO 10K 10. R66, R67 FROM 3.3K TO 10K 11. POWER SWITCH (AP2822AKATR-G1) CIRCUITS WERE ADDED AND R75, R76 REMOVED. 12. R64, R71, R87, R88 REMOVED
3.0	1. R29, R41 VALUE CHANGED FROM 20K TO 10K 2. R89, 0E RES REMOVED 3. R108, R107, C73 RESISTER AND CAPACITOR ADDED USB_P_PWR_P1 4. R109, R110, C74 RESISTER AND CAPACITOR ADDED USB_P_PWR_P2 5. U12, U13 PART CHANGED FROM TCK323G TO TPS2114APWR 6. CCG4 XRES PULL UP CHANGED FROM VDDD TO VDDIO AND RESISTOR(R68), CAPACITOR(C29) VALUE CHANGED AS 100K AND 1uF 7. D6-D9 AND R113-R118 ADDED IN ORING CIRCUITS. 8. J1 PART NUMBER CHANGED FROM "1734035-2" TO "548190519"
4.0	1. R103, R104 VALUES CHANGED FROM 10K TO 20K 2. R105, R106 CHANGED AS DNL AND R78, R79 AND R103, R104 DNL REMOVED.

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Title: **CCG4 DAUGHTER BOARD - REV HISTORY**

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